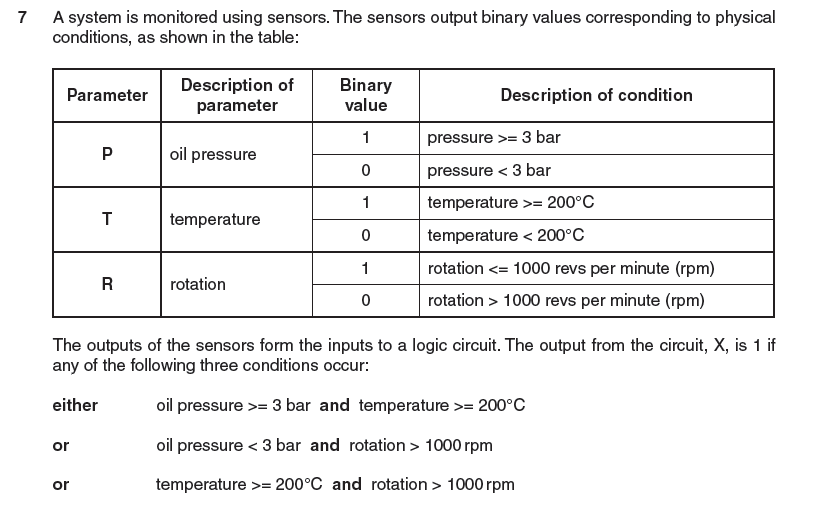
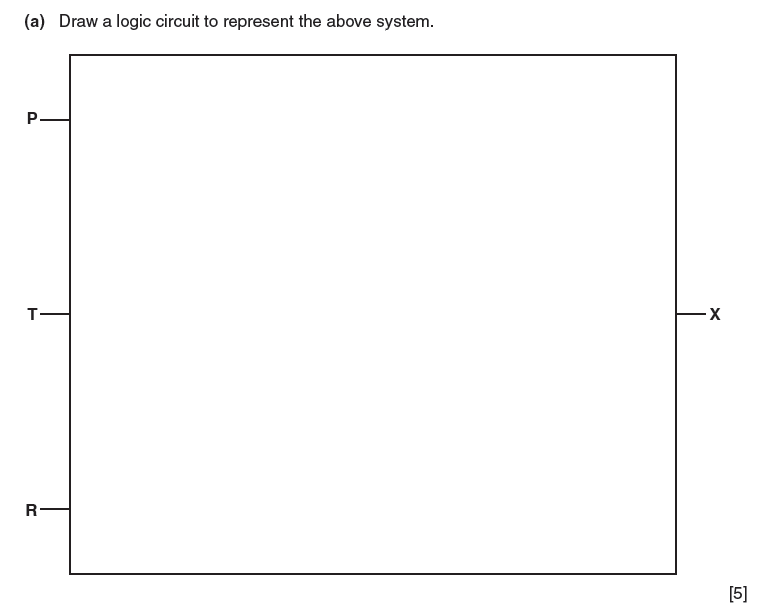
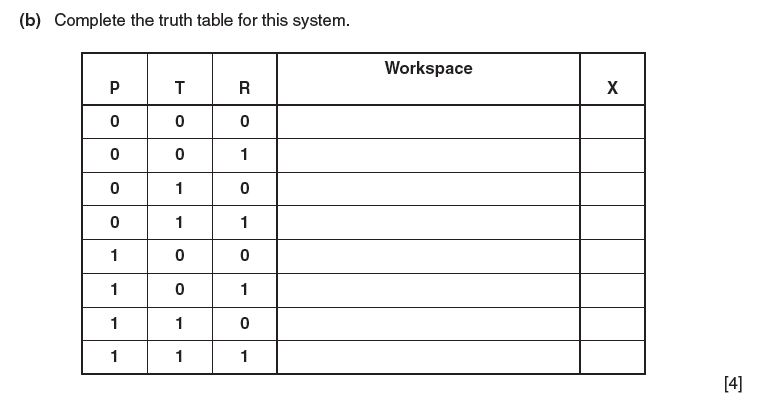
**Logic gate (2)**

**Name \_\_\_\_\_\_\_class\_\_\_\_\_\_**



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4. Implementing Circuits from Boolean Expressions and construct the truth table

* X = AC+BC’+A’BC

|  |  |  |  |
| --- | --- | --- | --- |
| A | B | C | X |
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|  |  |  |  |

* x = AB+B’C

|  |  |  |  |
| --- | --- | --- | --- |
| A | B | C | X |
|  |  |  |  |
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|  |  |  |  |

* x=(A+B)(B’+C)

|  |  |  |  |
| --- | --- | --- | --- |
| A | B | C | X |
|  |  |  |  |
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